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APPLICATION NO. FILING		ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/087,982	,	03/05/2002	Junichi Tanaka	500.41371X00	6965	
20457	7590	08/26/2003				
	,	RY, STOUT &	EXAMINER			
SUITE 1800		TEENTH STRE	HASSANZADEH, PARVIZ			
ARLINGTO	N, VA 2	2209-9889		ART UNIT	PAPER NUMBER	
				1763		
				DATE MAILED: 08/26/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

				M
		Applicati n No.	Applicant(s)	
		10/087,982	TANAKA ET AL.	
	Offic Action Summary	Examiner	Art Unit	
	<u> </u>	Parviz Hassanzadeh	1763	
Period fo	Th MAILING DATE of this communication app or Reply	pears on the cover sheet wi	th the correspondence address	
THE - Exte after - If the - If NC - Failu - Any	ORTENED STATUTORY PERIOD FOR REPL' MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a repl' period for reply is specified above, the maximum statutory period vire to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing and patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a re y within the statutory minimum of thirt vill apply and will expire SIX (6) MON , cause the application to become AB	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communication ANDONED (35 U.S.C. § 133).	on.
1)⊠	Responsive to communication(s) filed on 26 /	April 2002 .		
2a)□	<u> </u>	is action is non-final.		
3)	Since this application is in condition for allowed closed in accordance with the practice under	ance except for formal mat		is
Disposit	ion of Claims	•		
4)⊠	Claim(s) 1-10 is/are pending in the application).		
	4a) Of the above claim(s) is/are withdraw	wn from consideration.		
5) 🗌	Claim(s) is/are allowed.			
6)⊠	Claim(s) <u>1-10</u> is/are rejected.			
7) 🗌	Claim(s) is/are objected to.			
	Claim(s) are subject to restriction and/o ion Papers	r election requirement.		
	The specification is objected to by the Examine	г.		
·	The drawing(s) filed on <u>05 March 2002</u> is/are: a		ed to by the Examiner.	
,—	Applicant may not request that any objection to th		•	
11)	The proposed drawing correction filed on		, ,	
	If approved, corrected drawings are required in re	oly to this Office action.	•	
12) 🔲	The oath or declaration is objected to by the Ex	aminer.		
Priority (ınder 35 U.S.C. §§ 119 and 120			
13)[Acknowledgment is made of a claim for foreign	n priority under 35 U.S.C. §	§ 119(a)-(d) or (f).	
	☐ All b)☐ Some * c)☐ None of:		, ,,,,	
	1. Certified copies of the priority document	s have been received.		
	2. Certified copies of the priority document		pplication No.	
	3. Copies of the certified copies of the prio application from the International Bu	rity documents have been reau (PCT Rule 17.2(a)).	received in this National Stage	
	See the attached detailed Office action for a list	·		
	Acknowledgment is made of a claim for domesti	•	• • • • • • • • • • • • • • • • • • • •	tion).
)	• •		
Attachmen	•			
2) 🔲 Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s) _	5) Notice of I	Summary (PTO-413) Paper No(s) nformal Patent Application (PTO-152)	
				_

Application/Control Number: 10/087,982 Page 2

Art Unit: 1763

DETAILED ACTION

Specification

The disclosure is objected to because of the following informalities: on page 1, line 18, it is suggested to delete the second occurrence of "plasma; on page 3, line 1, it is suggested to delete "when the".

Appropriate correction is required.

Drawings

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(4) because: reference character "42" has been used to designate both "DATABASE COPY" and "COPY SERVER" in Fig. 2, see page 7, line 20; and

reference character "221" has been used to designate both "SENSOR DATA FILE" and "DATA MANAGEMENT TABLE" in Fig. 2, see page 7, lines 15-17. A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Figure 12 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 103

Application/Control Number: 10/087,982

Art Unit: 1763

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claims 1-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art (Fig. 12, pages 1-2) in view of Seaton et al (US Patent No. 5,591,299).

The admitted prior art (Fig. 12) teaches a data processing apparatus for a semiconductor manufacturing apparatus, comprising:

a semiconductor manufacturing apparatus (etching system 1 including etching chambers 11, 12) for executing processing for a wafer;

a data collecting semiconductor device (data collector 2 including data processor 21 and database 22) for collecting processing data generated in association with the processing.

The admitted prior art fails to teach a data copying semiconductor device for extracting the processing data collected in the data collecting semiconductor device and for producing a copy of the processing data.

Seaton et al teach a semiconductor processing system (Fig. 1B) comprising a system monitoring system including a floppy disk drive 24 for storing various aspects of operation of a

Page 4

processor 10; a supervisor computer 102 in communication with the processor 10 and including a hard drive 103 for storing the recipes for implementation by the processor; and an engineering computer 106in communication with a plurality of supervisor computers such as the supervisor computer 102 and including an archive computer 109a and associated archive disk drive 109b for archiving data capture files for longer periods of time. The data capture disk drive 109b comprises five hard drives each having 800 to 900 MB of disk space. Moreover, because the capture disk 109b is networked, it is accessible by any computer in the spray processor host system 100, the archive computer 109a periodically copies data capture files stored in transmitted to the supervisor computer 102. (column 4, line 57 through column 6, line 12).

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention to implement the archive computer mechanism as taught by Seaton et al in the apparatus of admitted prior art in order to periodically copy data capture files on a series of large capacity memory for later processing.

Further regarding claims 2, 4, 5: the engineering computer 106 is in communication with the archive hard drive 109b via archive computer 109a and is capable of analyzing data.

Further regarding claims 3, 6: capture disk 109b is networked (removable).

Further regarding claims 7-10: the archive hard drive 109b is accessible to a plurality of computers through network line 108, thus the data can be accessed and analyzed independently.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Application/Control Number: 10/087,982 Page 5

Art Unit: 1763

Kagoshima et al (US Patent Application Publication No. 2003/0003607 A1) teach a plasma process system (Fig. 11) including a processed result database S5 and a processing state signal database S3 both in communication with a correlation analyzer S6; and

Smith et al (US Patent No. 6,077,386) teach a plasma processing system (Fig. 6) including plasma monitor control unit 128 in communication with main control unit 58, a display 130 and one or more data entry device 132 (column 32, lines 34-51 and column 36, lines 30-52).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Parviz Hassanzadeh whose telephone number is (703)308-2050. The examiner can normally be reached on Tuesday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gregory Mills can be reached on (703)308-1633. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)308-0661.

P. Hamen Jr. M Parviz Hassanzadeh Primary Examiner Art Unit 1763

August 18, 2003